



N5013/N5014 FPGA SmartNIC Intel® Stratix® based

100Gigabit Quad port Ethernet Intel® Stratix® 10DX FPGA Based

Product Description

Silicom's N5013/N5014 FPGA SmartNIC Intel® Stratix® based is a high-performance programmable PCI-Express card based on the Intel® Stratix® 10 DX 2100 FPGA. The Intel® Stratix® 10 DX 2100 FPGA is an extremely powerful FPGA technology which also features on-chip 8GB HBM2 memory providing an unprecedent 512 GByte/s of total aggregate bandwidth.



The N5014 additionally features two Intel E810-CAM1 devices connected to the FPGA and can function as the primary data path to and from the host system. This adds the full set of features of the E810 to the host interaction and its interface options in virtualized systems.

N5013/N5014 FPGA SmartNIC Intel® Stratix® based Use Cases:

- Network Function Virtualization (NFV)
- Multi-Access Edge Computing (MEC)
- Cyber security
- High-Performance Computing
- Finance
- Mobile Access and Core Network

FB4CG@S10D21 FPGA SmartNIC N5013/N5014:

- N5013: 4xQFSP, PCIe Gen4, FPGA, QDR, DDR4, Intel® Max® 10
- N5014: 4xQFSP, PCIe Gen4, FPGA, QDR, DDR4, 2xE810-CAM1, PEX88048, Intel® Max® 10

Key Features

- Intel® Stratix ® 10 DX 2100 FPGA
- HBM2: 8GB (2x4 GB, internal)
- DDR4/ 32GByte+ECC (x18 devices)
- QDR-IV: 144Mbit
- Two Intel® e810-CAM1 NIC devices (N5014 only)
- Intel® Max® 10 Board Management Controller
- On-board power and temperature sensors
- FPGA controlled link and status LEDs
- Time Synchronization via IEEE 1588 (PTP) and SyncE
- Bracket mounted SMA for PPS/10MHz input and optional external Sync bracket with 4 x SMA for in/out of PPS and 10MHz



Technical Specifications

General Technical Specifications Adapters:					
IEEE standard	IEEE 802.3 100GbE				
Interfaces	 Physical interface: 4 x QSFP28 slots Multimode SR4 (850nm), Single Mode LR4 (1310nm) or Direct Attached Copper (Twinax) Supports 100G-SR4, 100G-LR4, 100G-Cu (Direct Attached) 				
PCI bus	 PCIe 4.0 x16 On N5014 the FPGA and both E810-CAM1's are connected to the PCIe connector via an on-board Broadcom 48x PCIe switch 				
FPGA Details	Intel® Stratix® 10 DX 2100 • 2.073M Logic Element Fabric • HBM2: 8GB (2x4 GB, internal) • Connected to the PCIe connector via on-board Broadcom 48x PCIe switch (N5014 only)				
NIC details (N5014 only)	Intel® Network Controller E810-CAM1 Connected to the PCIe connector via on-board Broadcom 48x PCIe switch NIC provides a hardware compatible interface with Linux drivers Supports Data Plan Development Toolkit (DPDK)				
Configuration	 Configuration flash can be made to support multiple boot images for automatic fallback to factory default image Upload of FPGA configuration to flash via PCIe – with supported image and tool 				
On-board Memory	 DDR4, 32GB +ECC (x18 devices), 2 banks of 16G + ECC. QDR-IV XP, 144Mbit HBM2, 8GB in Stratix 10 DX2100 				
On-board Clock	 PCIe clock: 100 MHz 8 output reprogrammable clock generator Supports network synchronization 				
Additional Board Support	 On-board power and temperature sensors (via SMBus/I2C) LEDs for board status and board management 				
Environment	 Full height, ¾ length, Dual Slot Passive Storage temperature: -40°C-65°C (-40°F-149°F) Operating temperature: 0°C - 45°C (32°F - 113°F) Hardware compliance: RoHS, FCC Class A, CE, UL 				
Thermal Design	 Passive heat sink targeted to dual slot Built-in thermal protection 				

Power	 Max 225W 75W max from the PCIe Slot 150W max from the 12V Aux 2×4 Connector Passive cooling 	
Port LEDs	 4 x Link/ ACT for the 4 x QSFP28, on bracket 1 x multi color status LED, on bracket 	
Time Synchronization	 Support Sync-E/ 1588 standard (optional feature depending on FPGA image) Bracket mounted SMA connector, optional Support for external Sync bracket with 4 x SMA for in/out of PPS and 10MHz 	
Board Management	 Intel® Max® 10 FPGA Board Management Controller Voltage level monitoring Thermal shut-down protection Over current protection on 12V input 	

Order Information

P/N	Description	Notes:	Intel name
FB4CGG2@S10D21- D00P0	Programmable PCI Express Server Adapter Based on Intel FPGA S10DX21	PCIe x16 Gen 4, 4x QSFP28, S10DX21, HBM 8GB D: 144MbitQDRIV (iTemp/933Mhz) +DDR4 2x16GBDDR4 (32GB) 0: (0) E810-CAM1 0: ¾ Length, 10" P: Passive heat sink, dual slot 0: No SMA	N5013
FB4CGG2@S10D21- D00P1	Programmable PCI Express Server Adapter Based on Intel FPGA S10DX21	PCIe x16 Gen 4, 4x QSFP28, S10DX21, HBM 8GB D: 144MbitQDRIV (iTemp/933Mhz) +DDR4 2x16GBDDR4 (32GB) 0: 0 x E810-CAM1 0: ¾ Length, 10" P: Passive heat sink, dual slot 1: 1x SMA/PPS + extra 4x SMA bracket	N5013

FB4CGG2@S10D21- D20P0	Programmable PCI Express Server Adapter Based on Intel FPGA S10DX21	PCIe x16 Gen 4, 4x QSFP28, S10DX21, HBM 8GB Broadcom 48x PCIe switch D: 144MbitQDRIV (iTemp/933Mhz) +DDR4 2x16GBDDR4 (32GB) 2: 2xE810-CAM1 0: 3/4 Length, 10" P: Passive heat sink, dual slot 0: 1x SMA/PPS	N5014
FB4CGG2@S10D21- D20P1	Programmable PCI Express Server Adapter Based on Intel FPGA S10DX21	PCIe x16 Gen 4, 4x QSFP28, S10DX21, HBM 8GB Broadcom 48x PCIe switch D: 144MbitQDRIV (iTemp/933Mhz) +DDR4 2x16GBDDR4 (32GB) 2: 2x E810-CAM1 0: ¾ Length, 10" P: Passive heat sink, dual slot 1: 1x SMA/PPS + extra 4x SMA bracket	N5014

1V4