



### fb4CGg3@VU series FPGA Card

#### Quad Port QSFP28 100 Gigabit Xilinx® Virtex Ultrascale

##### Product Description

The fb4CGg3@VU/VU+ series is a high performance OEM hardware platform intended for 10/40/25/50/100 Gigabit Ethernet via its quad QSFP28 slots.

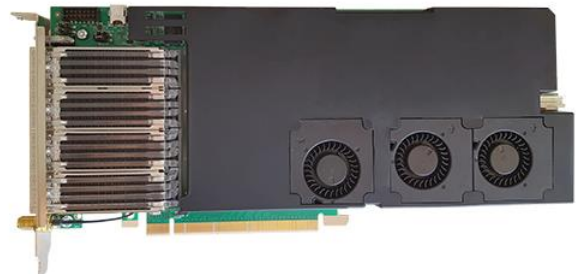
The standard configuration is based on the Xilinx® Virtex UltraScale+ VU9P FPGA, to provide ample capacity for the quad QSFP28 interface. The card is also offered with a variety of different FPGAs to provide flexibility for the intended application - this includes both Virtex Ultrascale and the Virtex Ultrascale+ series from Xilinx.

The card is mounted with 2 x 64-bit DDR4 2400MT/s 4GB for a total of 8 GB. In addition to this it features 2 SODIMM sockets for use with DDR4 or QDR11+ modules for low latency applications.



##### Key Features

- Xilinx® Virtex UltraScale+ VU9P FPGA (standard configuration)
- 4 x QSFP28 ports
- 2 x 64-bit DDR4@2400MT/s
- 2 x Optional SODIMM
- Configuration flash RAM for boot images
- PCIe form-factor: 3/4 length, standard height PCIe
- On-board power and temperature sensors
- FPGA controlled link and status LEDs
- Passive cooling (optional)



## Technical Specifications

<b>IEEE standard:</b>	IEEE 802.3 10GE, 40GE, 25GE, 100GE
<b>Interfaces:</b>	<ul style="list-style-type: none"> <li>Physical interface: 4 x QSFP28 slots</li> <li>2 port variant available</li> <li>Supports QSFP+/QSFP28 modules: including fan-out modules for 4x10G/4x25GE per slot, Multimode SR4 (850nm), singlemode LR4 (1310nm), singlemode PSM4 (1310nm), multimode LRM4 (1310 nm), or Direct Attached Copper (Twinax) and others</li> <li>Data rate: 16x10, 4x40, 16x25, 4x100 Gbps</li> </ul>
<b>Host Interface</b>	
<b>PCI bus:</b>	<ul style="list-style-type: none"> <li>16 lanes PCIe Gen1/Gen2/Gen3</li> <li>PCIe compliant</li> </ul>
<b>General Technical Specifications</b>	
<b>Configuration:</b>	<ul style="list-style-type: none"> <li>16-bit fast parallel programming interface from supporting preprogrammed controller</li> <li>Configuration flash supports two boot images with automatic fallback to fail safe image if first image fails</li> <li>Upload of FPGA configuration to flash via PCIe</li> <li>Support for encrypted FGPA bit file (optional)</li> </ul>
<b>On-board Memory:</b>	<ul style="list-style-type: none"> <li>2 x 64-bit DDR4@2400MT/s 4 GB</li> <li>2 x Optional SODIMM</li> <li>64-bit DDR4 @2400MT/s 4 GB</li> <li>36-bit QDRII+ @1266MT/s 288 Mb</li> <li>8 MB user configurable space in flash RAM for permanent storage</li> <li>256 MB Configuration flash RAM for boot images</li> </ul>
<b>On-board Clock:</b>	<ul style="list-style-type: none"> <li>PCIe clock: 100 MHz</li> <li>200 MHz clock</li> <li>50 MHz clock</li> <li>161.13 MHz clock</li> </ul>
<b>FPGA Details:</b>	<ul style="list-style-type: none"> <li>FPGA Xilinx® Virtex UltraScale+</li> </ul>
<b>Environment:</b>	<ul style="list-style-type: none"> <li>Physical dimensions: 3/4 length, standard height PCIe</li> <li>Power consumption: &lt;10W with uninitialized FPGA</li> <li>Operating temperature: 0 – 55°C, 30 – 130°F</li> <li>Operating humidity: 20 – 80%</li> <li>Hardware compliance: RoHS, CE</li> <li>Active cooling (heat sink with fan)</li> <li>Passive cooling (optional)</li> </ul>
<b>Additional Board Support:</b>	<ul style="list-style-type: none"> <li>On-board power and temperature sensors</li> <li>Board status LEDs</li> <li>User configurable dual color LED</li> <li>FPGA controlled Link and Activity LED for each port</li> <li>PPS clock synchronization connector</li> <li>PCIe AUX power connector and cable</li> </ul>
<b>Additional Intellectual Property Modules:</b>	<ul style="list-style-type: none"> <li>Flash configuration I/F</li> <li>2x8 lane PCIe</li> <li>DDR4 Memory controller</li> <li>I2C controller</li> <li>SmartNIC framework (optional)</li> <li>Ultra low latency 10GE MAC (optional)</li> <li>TCP-offload engine (optional)</li> </ul>
<b>Ordering Example:</b>	<p>fb4CGg3@VU09P-3-SS-1A 4-port card with the following configuration:</p> <ul style="list-style-type: none"> <li>VU09P Virtex UltraScale+ FPGA</li> <li>Speedgrade -3 (fastest available)</li> <li>288 Mb x36 SQIVe RAM mounted in SoDimm slot A</li> <li>288 Mb x36 SQIVe RAM mounted in SoDimm slot B</li> <li>SMA connector for PPS signal on front panel</li> <li>Heat sink with integrated UL/CE approved fan</li> </ul>

## Order Information

P/N	Description
fb4CGg3@VU080-2-00-1A	Virtex UltraScale VU80-2, No SODIMMs
fb4CGg3@VU080-2-DD-1A	Virtex UltraScale VU80-2, 2x4GB DDR4 SODIMMs
fb4CGg3@VU125-2-00-1A	Virtex UltraScale VU125-2, No SODIMMs
fb4CGg3@VU125-2-DD-1A	Virtex UltraScale VU125-2, 2x4GB DDR4 SODIMMs
fb4CGg3@VU190-2-00-1A	Virtex UltraScale VU190-2, No SODIMMs
fb4CGg3@VU190-2-DD-1A	Virtex UltraScale VU190-2, 2x4GB DDR4 SODIMMs
fb4CGg3@VU07P-2-00-1A	Virtex UltraScale+ VU7P-2, No SODIMMs
fb4CGg3@VU07P-2-DD-1A	Virtex UltraScale+ VU7P-2, 2x4GB DDR4 SODIMMs
fb4CGg3@VU09P-2-00-1A	Virtex UltraScale+ VU9P-2, No SODIMMs
fb4CGg3@VU09P-2-DD-1A	Virtex UltraScale+ VU9P-2, 2x4GB DDR4 SODIMMs
fb4CGg3@VU09P-2-RR-1A	Virtex UltraScale+ VU9P-2, 2xQDRII RAM (x36, 288Mb)
fb4CGg3@VU09P-3-00-1A	Virtex UltraScale+ VU9P-3, No SODIMMs
fb4CGg3@VU09P-3-DD-1A	Virtex UltraScale+ VU9P-3, 2x4GB DDR4 SODIMMs
fb4CGg3@VU09P-3-RR-1A	Virtex UltraScale+ VU9P-3, 2xQDRII RAM (x36, 288Mb)