



### PE310G4SPI9LA

#### Quad Port Fiber 10 Gigabit Ethernet PCI Express Server Adapter Intel® 82599ES Based

#### Product Description

Silicom's 10 Gigabit Ethernet PCI Express server adapters are designed for Servers and high-end appliances.

The Silicom 10 Gigabit Ethernet PCI Express Server adapters offer simple integration into any PCI Express X8 to 10Gigabit Networks. The performance is optimized so that system I/O is not the bottleneck in high-performance networking applications.

The Silicom 10 Gigabit Ethernet PCI Express server adapters are based on Intel 82599ES Ethernet controller with two fully integrated Gigabit Ethernet Media Access Control (MAC) and SFI ports.

In addition to managing MAC and PHY Ethernet layer functions, the controller manages PCI Express packet traffic across its transaction, link, and physical/logical layers. Using hardware acceleration, the controller offloads tasks from the host, such as TCP/UDP/IP checksum calculations and TCP segmentation.

Silicom's 10 Gigabit Ethernet PCI-Express Server adapters are the ideal solution for implementing multiple network segments, mission-critical high-powered networking applications and environments within high performance servers.



#### Key Features

##### SFP+ 10Gigabit Ethernet:

10Gigabit Ethernet Adapter with SFP cage support:

##### Copper 10SFP+Cu (Passive Direct Attach Cable):

- Compliant with the SFP+ MSA SFF-8431 specification
- Up to 10 meters

##### Fiber 10 Gigabit Ethernet 10GBASE-SR:

- 10BASE-SR with 10Gigabit 850nm Small form Factor Pluggable (SFP+)

##### Fiber 10 Gigabit Ethernet 10GBASE-LR:

- 10BASE-LR with 10Gigabit 1310nm Small form Factor Pluggable (SFP+)

**Host Interface:**

- PCI Express X8 lanes
- Support PCI Express Base Specification 3.0 (8GT/sec)
- Low power

**Performance Features:**

- IPV4 and IPV6 Supports for IP/ TCP and IP/UDP Receive Checksum offload
- Fragmented UDP checksum offload for Packet Reassembly
- CPU utilization- the 82599 supports reduction in CPU utilization, mainly by supporting Receive Side Coalescing (RSC)
- Support for 16 virtual machine Device Queues (VMDq) per port
- Support Direct Cache Access (DCA)
- Advanced memory architecture reduces latency by preceding TSO packets. A TSO packet may be interleaved with other packets going to the wire
- Minimized device I/O interrupts using MSI and MSI-X
- Offload of TCP / IP / UDP checksum calculation and TCP segmentation
- Large on chip receive packet buffer (512 KB)
- Large on chip transmit packet buffer (160KB)
- Supports the VPD (Vital Product Data) capability defined in the PCI specification ver. 3.0
- Time sync- IEEE1588- Precision Time Protocol (PTP)
- Supports the BCN (Backward Congestion Notification) protocol in addition to the EEDC functionality

**LAN Features:**

- IEEE 802.x flow control support
- IEEE 802.q VLAN tagging support
- Supports a mode where all received and sent packets have at least one VLAN tag in addition to the regular tagging
- IEEE 802.1p layer 2 priority encoding
- Jumbo Frame (up to 15.5KB)
- Link Aggregation and Load Balancing
- RFC2819 RMON MIB statistics
- TCP Segmentation Offload Up to 256KB
- Ipv6 Support for IP/TCP Receive Checksum Offload
- DDP Offload
- LEDs indicators for link/Activity and speed

### Security Features:

- IEEE P802.1AE LinkSec specification. It incorporates an inline packet crypto unit to support both privacy and integrity checks on a packet by packet basis. The transmit data path includes both encryption and signing engines. On the receive data path it includes both decryption and integrity checkers
- IPsec off load for a given number of flows
- Off-load IPsec for up to 1024 Security associations (SA) for each of TX and RX
- AH and ESP protocols for authentication and encryption
- AES-128-GMAC and AES-GCM crypto engines
- Transport mode encapsulation

### Technical Specifications

SFP+ 10Gigabit Ethernet Technical Specifications Adapters:	
<b>SFP+ (Small Form Factor Pluggable) supports:</b>	SFI interfaces supports 10GBase-R PCS and 10 Gigabit PMA in order to connect with SFP+ to 10GBase-SR // 1000Base-SX / 10GBase-LR and SFP+ Direct Attach
<b>10GBase-SR SFP+: IEEE Standard / Network topology:</b>	Fiber 10Gigabit Ethernet, 10GBASE-SR (850nm LAN PHY)
<b>10GBase-SR SFP+: Data Transfer Rate :</b>	10.3125GBd
<b>10GBase-SR SFP+: Cables and Operating distance Up to:</b>	62.5um, 160MHz/Km 26m 62.5um, (OM1)200MHz/Km 33m 50um, 400MHz/Km 66m 50um, (OM2)500 MHz/Km 82m 50um, (OM3)2000MHz/Km 300m
<b>10GBase-LR SFP+: IEEE Standard / Network topology:</b>	Fiber 10Gigabit Ethernet, 10GBASE-LR (1310nm LAN PHY)
<b>10GBase-LR SFP+: Data Transfer Rate:</b>	10.3125GBd
<b>10GBase-LR SFP+: Cables and Operating distance Up to:</b>	Single-Mode: 10000m at 9um

<b>10GSFP+Cu :</b> <b>IEEE Standard / Network topology:</b>	Copper 10Gigabit Ethernet, 10GSFP+Cu (Direct Attach)
<b>1000Base-SX / 10GBase-SR SFP+:</b> <b>IEEE Standard / Network topology:</b>	Fiber Gigabit Ethernet, 1000Base-SX (850nm LAN PHY) Fiber 10Gigabit Ethernet, 10GBASE-SR (850nm LAN PHY)
<b>1000Base-SX / 10GBase-SR SFP+:</b> <b>Data Transfer Rate :</b>	10.3125GBd / 1.25GBd
<b>10000Base-SX / 10GBase-SR SFP+:</b> <b>Cables and Operating distance Up to:</b>	<b>10000Base-SX:</b> 62.5um, 160MHz/Km 220m 62.5um, (OM1)200MHz/Km 275m 50um, 400MHz/Km 500m 50um, (OM2)500 MHz/Km 550m 50um, (OM3)2000MHz/Km >550m <b>10GBase-SR:</b> 62.5um, 160MHz/Km 26m 62.5um, (OM1)200MHz/Km 33m 50um, 400MHz/Km 66m 50um, (OM2)500 MHz/Km 82m 50um, (OM3)2000MHz/Km 300m
<b>1000Base-LX / 10GBase-LR SFP+:</b> <b>IEEE Standard / Network topology:</b>	Fiber Gigabit Ethernet, 1000Base-LX (1310nm LAN PHY) Fiber 10Gigabit Ethernet, 10GBASE-LR (1310nm LAN PHY)
<b>1000Base-LX / 10GBase-LR SFP+:</b> <b>Data Transfer Rate :</b>	10.3125GBd / 1.25GBd
<b>1000Base-LX / 10GBase-LR SFP+:</b> <b>Cables and Operating distance Up to:</b>	<b>10000Base-LX:</b> Single-Mode: 5000m at 9um <b>10GBase-LR:</b> Single-Mode: 10000m at 9um
<b>-SRD: Fiber 1000BASE-SX / 10GBASE-SR Technical Specifications:</b>	
<b>Optical Output Power (1G):</b>	Minimum: -9.5 dBm

<b>Optical Receive Sensitivity (1G):</b>	Maximum: -17 dBm
<b>Maximum Input Power (1G):</b>	Maximum: +0.5dBm
<b>Output Transmit Power (10G):</b>	Typical: -2.6 dBm Minimum: -5 dBm
<b>Optical Receive Sensitivity (10G):</b>	Typical: -15.9 dBm Maximum: -11.1 dBm
<b>Maximum Input Power (10G):</b>	Maximum: +0.5dBm
<b>-LRD: Fiber 1000BASE-LX / 10GBASE-LR Technical Specifications:</b>	
<b>Optical Output Power (1G):</b>	Minimum: -11 dBm
<b>Optical Receive Sensitivity (1G):</b>	Maximum: -19 dBm
<b>Maximum Input Power (1G):</b>	Maximum: +0.5dBm
<b>Output Transmit Power (10G):</b>	Typical: -1.98 dBm Minimum: -8.2 dBm
<b>Optical Receive Sensitivity (10G):</b>	Typical: -18.7 dBm Maximum: -12.5 dBm
<b>Maximum Input Power (10G):</b>	Maximum: +0.5dBm
<b>Operating Systems Support</b>	
<b>Operating system support:</b>	Windows Linux
<b>General Technical Specifications</b>	
<b>Interface Standard:</b>	PCI-Express Base Specification Revision 3.0 ( 8 GTs)
<b>Board Size:</b>	Low profile short add-in card: 165.15mm X 68.91mm (6.502"X 2.713")
<b>PCI Express Card Type:</b>	X8 Lane
<b>On Board Connector Voltage:</b>	+12V +- 8%

<b>PCI Connector:</b>	X8 Lane
<b>Controller:</b>	2 X Intel 82599ES
<b>Holder:</b>	Metal Bracket
<b>Weight:</b>	170gr
<b>Power Consumption (SR):</b>	18W, 1.5A at 12V: Typical all ports operate at 10Gb/s, 17.64W, 1.47A at 12V: Typical No link at all ports
<b>Power Consumption (LR):</b>	18.12W, 1.51A at 12V: Typical all ports operate at 10Gb/s, 17.64W, 1.47A at 12V: Typical No link at all ports
<b>Operating Temperature:</b>	0°C – 45°C (32°F – 113°F)
<b>Air Flow Requirements:</b>	200 ft./min
<b>Storage:</b>	-40°C–65°C (-40°F–149°F)
<b>EMC Certifications:</b>	<p>FCC Part 15, Subpart B Class A</p> <p>Conducted Emissions</p> <p>Radiated Emissions</p> <p>CE EN 55022: 1998 Class A Amendments A1: 2000; A2: 2003</p> <p>Conducted Emissions</p> <p>Radiated Emissions</p> <p>CE EN 55024: 1998 Amendments A1: 2000; A2: 2003</p> <p>Immunity for ITE Amendment A1: 2001</p> <p>CE EN 61000-3-2 2000, Class A</p> <p>Harmonic Current Emissions</p> <p>CE EN 61000 3-3 1995, Amendment A1: 2001</p> <p>Voltage Fluctuations and Flicker</p> <p>CE IEC 6100-4-2: 1995</p> <p>ESD Air Discharge 8kV. Contact Discharge 4kV.</p> <p>CE IEC 6100-4-3:1995</p> <p>Radiated Immunity (80-1000Mhz), 3V/m 80% A.M. by 1kHz</p> <p>CE IEC 6100-4-4:1995</p> <p>EFT/B: Immunity to electrical fast transients 1kV Power Leads, 0.5Kv Signals Leads</p> <p>CE IEC 6100-4-5:1995</p> <p>Immunity to conductive surges COM Mode; 2kV, Dif. Mode 1kV</p> <p>CE IEC 6100-4-6:1996</p> <p>Conducted immunity (0.15-80 MHz) 3VRMS 80% A.M.</p>

	<p>By 1kHz  CE IEC 6100-4-11:1994  Voltage Dips and Short Interruptions  V reduc &gt;95%, 30% &gt;95% Duration 0.5per, 25per, 250per</p>
<b>MTBF*:</b>	<p>36 years  * According to Telcordia SR-332 Issue 2. Environmental condition – GB (Ground, Fixed, and Controlled). Ambient temperature 40°C</p>
<b>LEDs</b>	
<b>LEDs:</b>	<p>(2) LEDs per port  Upper LED : Link Speed:  Turns on Blue 10G Link.  Turns on Yellow 1G Link  Lower LED: Link/Act :  Turns on link (Green),  Blinks on activity (Green)</p>
<b>LEDs location:</b>	<p>LEDs are located on the PCB, visible via holes in the metal bracket. Each Green Link/Act and LED and Yellow/ Blue Link Speed LED is located above its own SFP connector port by light pipes.</p>
<b>Connectors:</b>	<p>(4) SFP+ cage</p>

## Order Information

P/N	Description	Notes
<b>PE310G4SPi9LA-XR</b>	Quad Port SFP+ 10 Gigabit Ethernet PCI Express Server Adapter	X8 Gen3 , Low Profile, Based on Intel 82599ES, Support Direct Attached Copper cable, Support Silicom SFP+ approved transceiver. RoHS compliant
<b>PE310G4SPi9LA-SR</b>	Quad Port Fiber (SR) 10 Gigabit Ethernet PCI Express Server Adapter	X8 Gen3, Based on Intel 82599ES, Low-profile, on board support for Fiber SR, RoHS compliant
<b>PE310G4SPi9LA-LR</b>	Quad Port Fiber (LR) 10 Gigabit Ethernet PCI Express Server Adapter	X8 Gen3, Based on Intel 82599ES, Low-profile, on board support for Fiber LR, RoHS compliant
<b>PE310G4SPi9LA-A-XR</b>	Quad Port SFP+ 10 Gigabit Ethernet PCI Express	X8 Gen3 , Low Profile, Based on Intel 82599ES,

	Server Adapter	Support Direct Attached Copper cable, Support Silicom SFP+ approved transceiver. RoHS compliant. I/O mapping enabled
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Model P/N -LP /

-LP: Assemble Low Profile Metal Bracket

-A: I/O mapping enabled

-E: PXE Enable

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